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MAY 17 2004

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: T. ISHII  
Application No.: 09/764,487  
Filed: January 18, 2001  
For: SEMICONDUCTOR DEVICE HAVING DIFFERENT TYPES  
OF MEMORY CELL ARRAYS STACKED IN A VERTICAL  
DIRECTION (As Amended)  
Art Unit: 2824  
Examiner: A. Q. Tran

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

May 17, 2004

Sir:

In response to the Office Action dated December 16, 2003, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Claims; and**

**Remarks are included following the amendments.**